

FIG. 1  $\alpha$ 

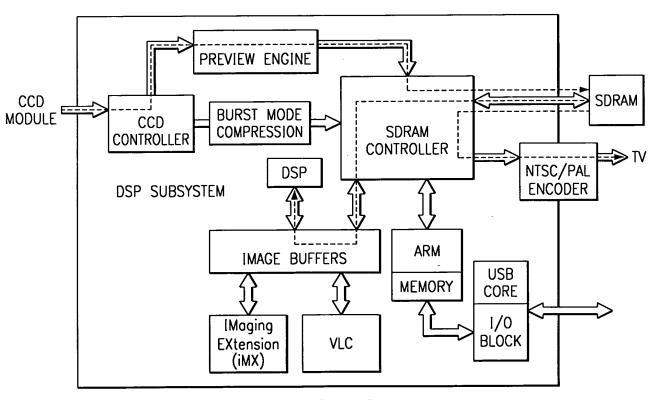
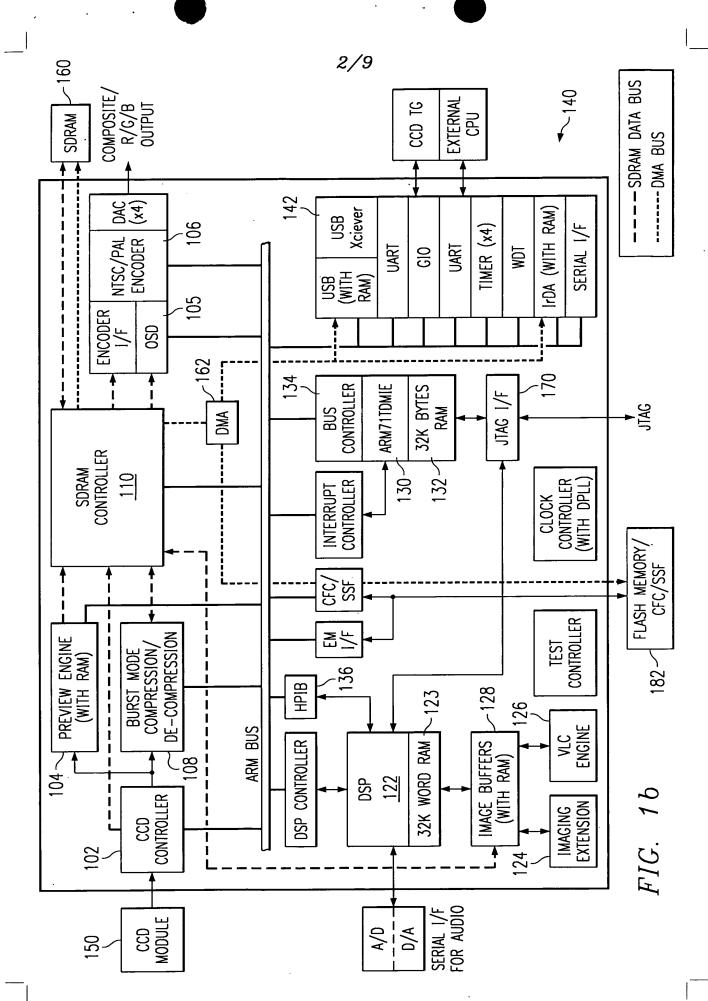
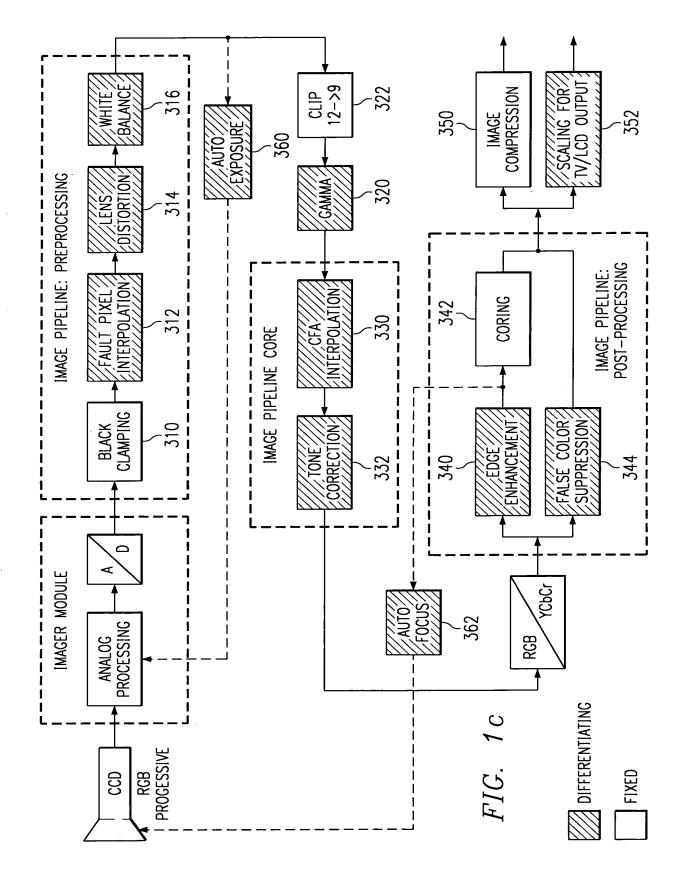
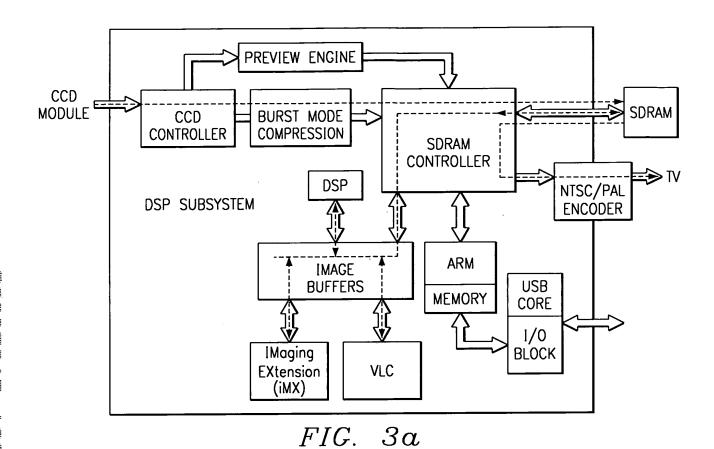


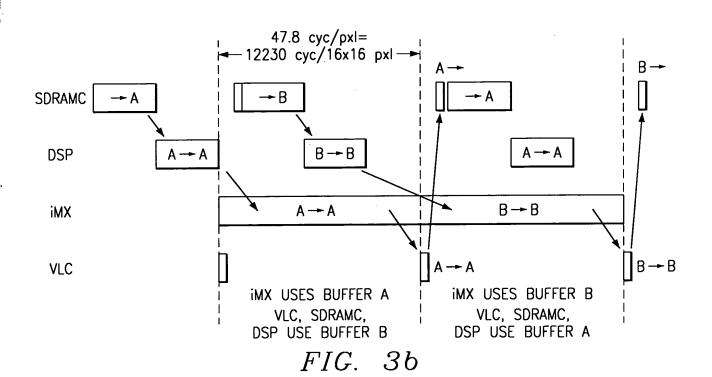
FIG. 2





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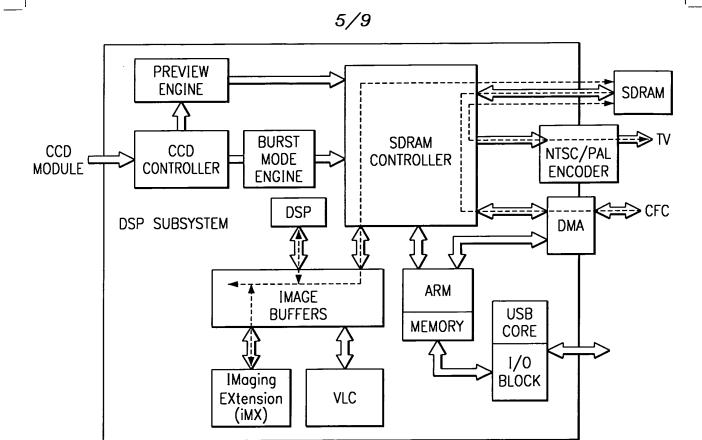


FIG. 4

PREVIEW ENGINE CCD MODULE " **SDRAM** BURST MODE CCD COMPRESSION CONTROLLER **SDRAM** CONTROLLER **DSP ≒≫**⊺∨ NTSC/PAL DSP SUBSYSTEM **ENCODER ARM IMAGE BUFFERS** USB **MEMORY CORE** 1/0 **IMaging BLOCK EXtension VLC** (iMX)

FIG. 5

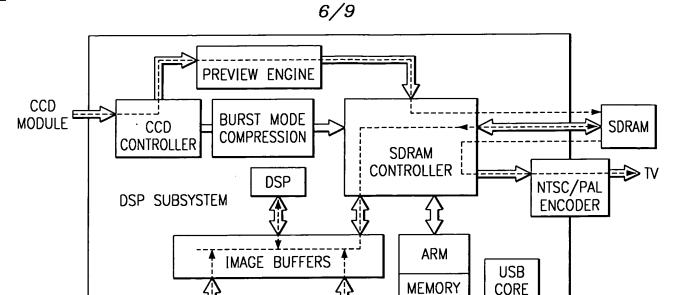


FIG. 6

**VLC** 

R	G	R	G
G	В	G	В
R	G	R	G
G	В	G	В

**IMaging** 

EXtension (iMX)

FIG. 7a

Ye	Су	Ye	Су
G	Mg	G	Mg
Ye	Су	Ye	Су
G	Mg	G	Mg

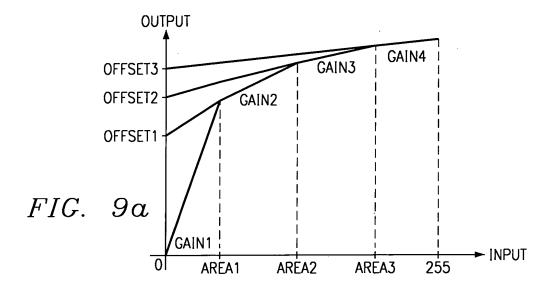
1/0

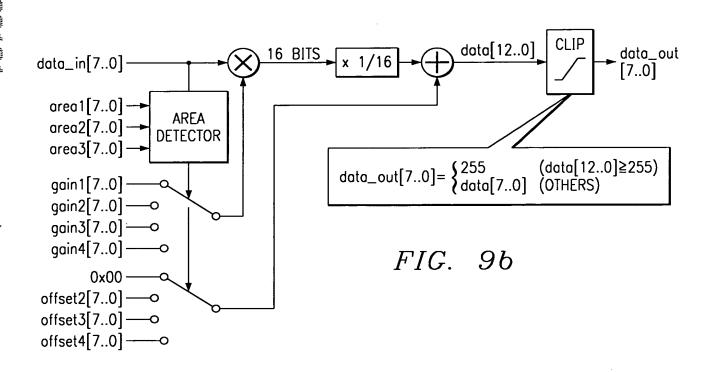
**BLOCK** 

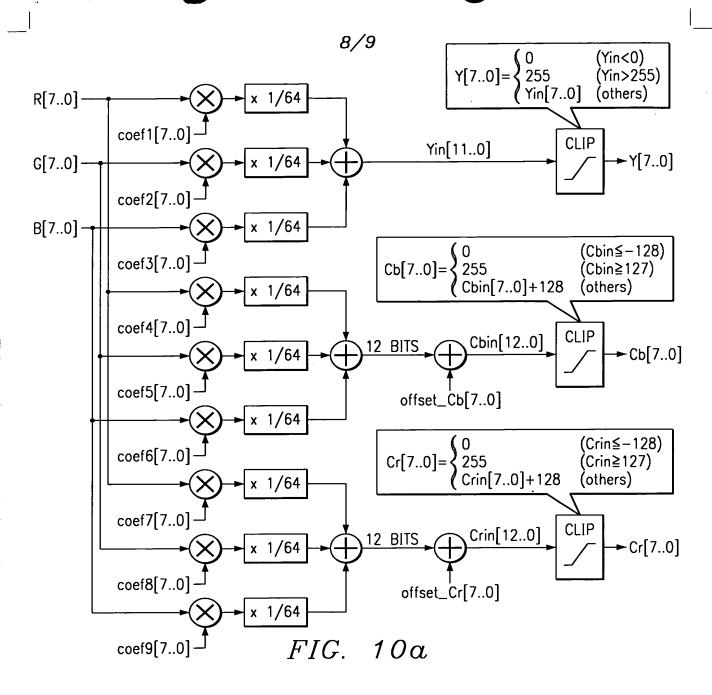
FIG. 7b

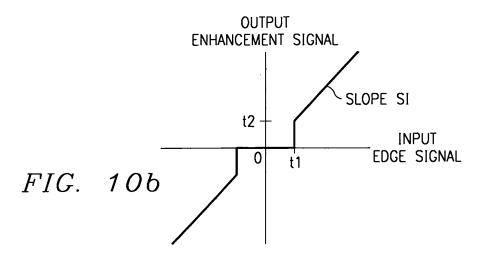
```
If (IN[17]=1 \text{ or } IN[16]=1) then OUT[9..0]=0x3FF;
                                             If( IN[17]=1 or IN[16]=1 or IN[15]=1) then
                                                OUT[7..0] = 0xFF;
                                             else
                                                OUT[7..0]=IN[14..7];
          OUT[9..0]=IN[15..6];
                                             end if;
       end if;
  data[9..0]
                                                                             CLIP
                             CLIP
                                   10 BITS /
                                                                  18 BITS.
                  18 BITS.
                                                                                       data_out
                                                   18 BITS
                                                                                       [7..0]
PVGAIN[7..0]
                                                   0x00010
                    gain 1[7..0] —
                                                white_balance_gain[7..0]
                    gain2[7..0]—0
                    gain3[7..0]—o
                    gain4[7..0]——
                                                 FIG. 8
              qain_selector_sw
```

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FIG. 11a

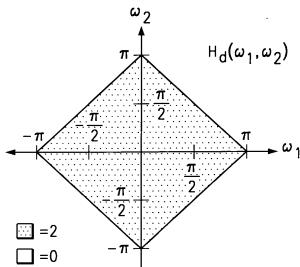


FIG. 11b

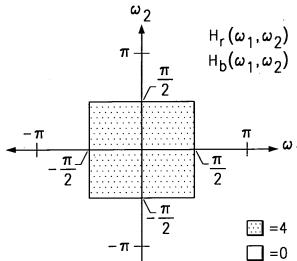


FIG. 11c

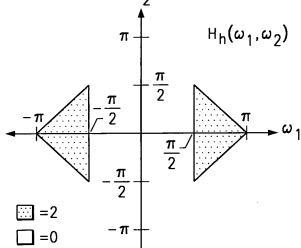


FIG. 11d

